

PATENT #9/Ext(1)

I hereby certify that this paper is being facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below.

Date

June 16, 1999

Edward W. Bulchis

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Leland R. Nevill
Application No. : 08/857,100
Filed : May 15, 1997
For : METHOD AND APPARATUS FOR IDENTIFYING
INTEGRATED CIRCUITS

Examiner : Daniel St-Cyr
Art Unit : 2876
Docket No. : 660073.555
Date : June 16, 1999

Assistant Commissioner for Patents
Washington, DC 20231

PETITION FOR AN EXTENSION OF TIME
UNDER 37 C.F.R. § 1.136(a)

Sir:

Applicant herewith petitions the Assistant Commissioner of Patents under 37 C.F.R. § 1.136(a) for a one-month extension of time for filing the response to the Examiner's Action dated February 25, 1999, from May 25, 1999 to June 25, 1999. Please charge my Deposit Account No. 19-1090 in the amount of \$110 to cover the cost of the extension.

Any deficiency or overpayment should be charged or credited to Deposit Account No. 19-1090. This petition is being submitted in triplicate.

Respectfully submitted,
Leland R. Nevill
SEED and BERRY LLP

Edward W. Bulchis
Registration No. 26,847

06/18/1999 BEVANS 00000003 191090 08857100

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EWB:ics
Enclosures:

Two copies of this Petition

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PATENT

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